

§9.3 #1, 6, 14

1 a)

Input	0 1 1 0 1 1 0 1 0
State	$S_0 S_0 S_3 S_4 S_1 S_4 S_2 S_2 S_1 S_0$
output	0 0 0 1 1 1 1 1 0

b)

Input	a b c c a a b
State	$S_0 S_2 S_0 S_3 S_0 S_2 S_2 S_0$
output	a a a c a a a a

c)

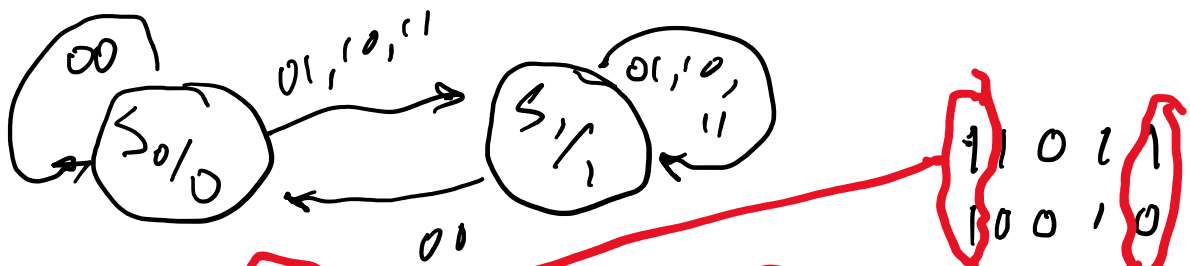
Input	0 1 0 0 1 1 0
State	$S_0 S_1 S_3 S_1 S_2 S_0 S_3 S_1$
output	0 0 1 0 0 1 1 0

6

State	Next State			
	a	b	c	
S_0	S_0	S_1	S_2	0
S_1	S_0	S_2	S_2	1
S_2	S_0	S_2	S_1	0

Input	a	c	b	a	b	c
State	S_0	S_0	S_2	S_2	S_0	S_1
Output	0	0	0	0	0	1

#14 a. Inputs : 00 01, 10 11



Input	10	10	01	10	10
State	S_0	S_1	S_1	S_0	S_1
Output	0	1	1	0	1

State	Next State				Output
	00	01	10	11	
S_0	S_1	S_1	S_1	S_1	0
S_1	S_0	S_1	S_1	S_1	1

